# The WEST project: Validation program for WEST tungsten coated plasma facing components

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**Abstract.** The W-for tungsten-Environment in Steady-state Tokamak (WEST) project is based on an upgrade of the Tore Supra tokamak from a carbon limiter to an X-point divertor device. A new set of actively cooled tungsten coated plasma facing components (PFC) will cover a part of the vessel to provide a fully metallic environment. This paper deals with the validation program performed for tungsten coatings ( $\geq 15 \,\mu$ m) on a CuCrZr substrate. The first step was dedicated to the qualification under high heat flux tests of the coating on small inertially cooled samples. To study the thermal behaviour and the non-uniformity, the second step was dedicated to the validation of the coating on large inertially cooled samples with geometry and shape (540 x 120 mm) representative of the WEST coated components. The last step was dedicated to the optimization of the coating and to the HHF tests up to 10.5 MW/m<sup>2</sup> on relevant coated actively cooled prototypes. Non-uniformity and thickness of the coating (15  $\mu$ m and 30  $\mu$ m) correspond to specifications. As no delamination was observed, coatings of 15 and 30  $\mu$ m were qualified with regard to their application on WEST coated components. In order to decrease the risk of coating delamination under thermal loading, it was decided to cover the upper divertor and baffle targets with the thinnest coating option of 15  $\mu$ m.

### **1. Introduction**

The WEST (W -for tungsten- Environment in Steady-state Tokamak) project is targeted to minimize risks for ITER full tungsten divertor procurement and operation [1]. It is based on an upgrade of the Tore Supra tokamak from a carbon limiter to an X-point divertor device, while taking advantage of its long discharge capability in the range of minutes. ITER-like actively cooled tungsten targets (monoblocks) will be integrated in the lower divertor and a new set of actively cooled tungsten coated plasma facing components (PFC) (e.g. upper divertor, baffle, protection against Vertical Displacement Events (VDE) and ripple losses) will cover parts of the vessel to provide a fully metallic environment (Fig. 1). This project will deal with the installation and testing of ITER-like actively cooled components [2]. The upper divertor target and the baffle are actively cooled PFCs. They are based on a Copper-Chromium-Zirconium (CuCrZr) heat sink concept, coated with tungsten. Since 2012, coating developments have been carried out for these components. This paper deals with the validation program performed for tungsten coatings ( $\geq$ 15 µm) on a CuCrZr substrate. It first describes the actively cooled plasma facing components involved, in terms of geometry and thermal loading specifications. Then, the main features and expected characteristics of this tungsten coating are presented. The last part is dedicated to the validation program of the W coating, mainly based on its thermal behavior assessment under high heat flux testing with regard to plasma facing components specifications.



Fig. 1: Tore Supra in the WEST configuration, showing the main plasma facing components

# 2. Upper divertor and baffle targets

# 2.1 Geometry

The WEST upper divertor and baffle targets consist of 12 independent toroidal sectors of  $30^{\circ}$  [3]. Each sector is composed of 38 copper-Plasma Facing Units (so called Cu-PFU in this paper) for the upper divertor target [4] and of 12 baffle units for the baffle target. These components are actively cooled (inlet pressure and coolant temperature are set to 33 bar and 70°C, respectively and the coolant velocity is 5 m/s). Each of these components is composed of a CuCrZr heat sink. CuCrZr is chosen for its advantages with regard to its thermal and mechanical properties. The heat loaded surface of these components is coated with a W layer [5]. For better adhesion of the W coating, a chamfer (R=2 mm) is machined along the length on the component edge (Fig. 2) The total Cu-PFU dimensions are ~ 540 x 32 x 42 mm, the surface to be coated is ~ 449 x 32 mm (Fig. 2). The baffle unit dimensions are ~ 405 x 121 x 18 mm, the surface to be coated is ~ 405 x 121 mm (Fig. 2).



Fig. 2: Geometries of a) Cu-PFU and b) baffle unit

### 2.2 Specification

The upper divertor target is designed to exhaust in total 4 MW of conducted power in steady state with a maximum local heat load of 8 MW/m<sup>2</sup> [4]. The baffle target must be able to extract in total 3 MW in steady state, in order to cope with the plasma conducted power, localized ion losses or fast electrons generated by the RF heating systems as well as radiation losses. The baffle target must be able to handle locally heat loads up to 3 MW/m<sup>2</sup> near the leading edge region (i.e. round edge shape region) in steady state. Components have also to sustain transient thermal loads (~400kJ/m<sup>2</sup>) [6] as the one occurring during WEST Edge Localised Modes (ELMs).

One of the constraints of these components, for cumulated long lasting plasma operation, is the maximum allowable surface temperature of  $450^{\circ}$ C for CuCrZr substrate. This temperature has been defined in order to keep the mechanical properties during operation life time [7]. However, during transient thermal loads, a surface temperature up to  $600^{\circ}$ C is tolerated.

#### **2.3 Coating characteristics**

The W coating thickness has been defined with regard to component specification and coating manufacturing feasibility. With respect to the component specification, the tungsten coating thickness is a compromise between the coating thermal performance (thin coatings to avoid delamination under thermal stresses) and the coating lifetime (thick coatings to handle plasma erosion). In the case of the upper divertor and

the baffle, the emphasis has been set on the performance under thermal stress, so that thin coatings (between 15 and 50  $\mu$ m) have been investigated during the validation process.

Rough estimates of the coating lifetime can be derived from the plasma edge simulations performed in [8] with the SolEdge2D-EIRENE code [9] [10]. Tungsten erosion both from the deuterium plasma and deuterium charge exchange atoms is calculated around the main chamber for typical discharge conditions in a lower single null configuration. A maximum gross erosion of  $1.5 \ 10^{-3} \ \mu m/s$  is found on the lower divertor in the strike point area, while it is an order of magnitude lower for the upper divertor. For the baffle, the maximum is found to be around  $5 \ 10^{-4} \ \mu m/s$  in the leading edge area. However, net erosion is expected to be significantly lower (typically around  $10^{-4} \ \mu m/s$  for the lower divertor and  $5 \ 10^{-5} \ \mu m/s$  for the baffle), as prompt redeposition will play a significant role. For a 15  $\mu m$  coating, this would correspond to a lifetime of several hours of plasma operation if gross erosion is taken as an upper envelop, and several tens of hours if net erosion is considered. In this last case, this would be sufficient for several years of experimental campaigns in WEST. However, it should be noted that the numbers quoted here are subject to large uncertainties, as the impact of light impurities (oxygen, carbon, etc) or ELMs, which are known to play a large role in W dust production in AUG or JET [11], is not taken into account in the present modelling. The estimated erosion rates here are however consistent with experimental findings in AUG, showing a net erosion of  $1.2 \ 10^{-4} \ \mu m/s$  in the divertor strike point area [11].

In order to avoid a difference of thermal behavior, non-uniformity of the deposit thickness has to be as low as possible (i.e. in the range of  $\pm$  5 µm around the specified nominal W coating thickness). During the coating series production, nominal tungsten thickness will be defined at + 5 µm / - 0 µm around specified coating thickness. With regard to plasma operation, impurities (C, O) concentration in coatings has to be as low as possible. In this study, the admitted concentration is 5% at.

# 3. Coating technique and validation

## 3.1 Description of PVD coating technique

Considering the specified coating thickness (15  $\mu$ m to 50  $\mu$ m), Physical Vapour Deposition (PVD) was studied for the realization of the W coating. W coatings were made by magnetron cathodic sputtering process in a vacuum chamber. The volume of this vacuum chamber is 0.6 x 0.6 m<sup>3</sup>. The coating on rounded edges and on Cu-PFU length (~ 449 mm) required a development in order to define adapted number and position of the cathodes in the PVD chamber. Finally, cathodes were placed facing each other on the walls of the reactor while the samples are placed on a rotating substrate.

Coating process is composed of several phases. The cleaning of substrate and tungsten targets with argon ions is the most important step to ensure the adherence of the coating. This phase consists in the removal of the oxides and particles from the surface to be coated.

The global coating process has been consequently adapted so that coating adherence under heat loads and coating homogeneity are ensured for both the heat flux incident flat surface and the rounded edge shape. In this configuration, no interlayer is needed between W coating and CuCrZr substrate.

#### 3.2 Validation program

The thermal behaviour of the coating during plasma operation depends mainly on its temperature but also on the temperature field in the coating itself. The temperature field is dependent on the heat loading on incident coated surface and on geometry/cooling of studied components. In order to assess thermal behaviour of the coating, a validation program has been performed. This program, divided in several phases, was mainly based on High Heat Flux (HHF) tests. The first phase was dedicated to HHF tests on small inertially cooled samples. The aim of this phase was to test the adherence of the coating onto CuCrZr substrate under relevant temperatures (between 450°C and 600°C), but not under relevant heat load. The objective of this phase was to provide a rough estimation, on a limited number of samples, about the thermal behaviour of samples equipped with the coating with needed specifications. The second step was dedicated to the validation of the coating on large inertially cooled samples with geometry and shape representative of the WEST coated components. The aim of the last step was to test prototypes with tungsten optimized coating under relevant WEST heat loading. High cycle fatigue experiments were excluded due to the time constraints of the WEST project. Therefore, a program for low-cycle fatigue experiments (130–500 cycles) was proposed. The number of cycles was chosen to be roughly the same as the one used for the qualification program for the W coating of JET ITER-like wall [12].

#### 3.2.1 Small scale inertially cooled samples

W coatings were developed and processed on small CuCrZr samples (diameter 30 mm, thickness 4 mm). HHF tests were performed in the neutral beam facility GLADIS at IPP Garching [13], on 4 small samples (2 samples with a coating thickness of 15  $\mu$ m, and 2 samples with a coating thickness of 50  $\mu$ m). Condition of Gaussian heat load repartition is recalled in [13]. The loading was performed to achieve a surface temperature of 450°C ( $\pm$ 50°C), a shutdown of the beam, a resting period for allowing the sample temperature to decrease to room temperature and a repetition of the before mentioned steps for 130 cycles. Due to the low coating thickness with regard to the total thickness of the tested samples, thermal gradient in the coating is negligible compared to the thermal gradient in the sample itself. Due to the adherence of the coating on the CuCrZr substrate, thermal resistance is negligible at this interface. Consequently, the maximum surface temperature is considered as the same as the maximum temperature in CuCrZr. The absorbed heat flux in these experiments was ~2.5 MW/m<sup>2</sup> for a loading time of 3 s. The two samples with 15  $\mu$ m coating thickness and one sample with 50  $\mu$ m coating thickness delaminated after the second cycle. As consequence, the 50  $\mu$ m thick coating was abandoned and a coating of 30  $\mu$ m was developed.

Additional HHF tests were performed in order to increase the number of cycle to 500 cycles and to load small samples at higher surface temperature relevant to transient heat loads (600°C). These tests were performed on two small samples (1 sample of 15  $\mu$ m, and 1 sample of 30  $\mu$ m) with the electron beam facility JUDITH 2 at Forschungszentrum Jülich (Germany). Condition of homogeneous heat load repartition is recalled in [14]. The loading time, loaded heat flux and the number of cycles are presented in Table 1. Samples were tested up to 5.9 MW/m<sup>2</sup>. As no delamination was observed, the 15  $\mu$ m and 30  $\mu$ m coating thicknesses were validated for small scale inertially cooled samples.

Samples	Thickness (Number of tested samples)	Number of cycles	Maximum surface temperature (°C)	Absorbed heat flux (MW/m2)	Loading time (s)/ Cooling time (s)	Result
Small	15 μm (2), 50 μm (2)	130	450°C	$2.5\pm5\%$	3 s / 20 s	1 sample (50 μm) delaminates
	15 μm (1), 30 μm (1)	500	450°C	$4.9\pm10~\%$	2 s / 20 s	No delamination
		Followed by 130	600°C	$5.9\pm10~\%$	2 s / 30 s	No delamination
Large	15 µm (1)	130	450°C	$3.4\pm20~\%$	3 s / 180 s	No delamination
	30 µm (1)	130	450°C	1.5 ± 5 %	8 s / 180 s	No delamination

Table 1 : Experimental data of High Heat Flux tests performed on small and large samples

### 3.2.2 Large scale inertially cooled samples

Then, coatings of 15  $\mu$ m and 30  $\mu$ m were realized on two large inertially cooled components (540x120x16 mm) with leading edge shape (Fig. 3). This geometry is representative of the upper divertor length as well as the baffle regarding the width and the leading edge shape. The coating thickness along the component length and the round edge shape was measured. For these coatings a non-uniformity of  $\pm 5 \,\mu$ m is measured. This result is in agreement with required specification (see section 2.3).

Large inertially cooled components were tested under HHF tests at JUDITH 2. The loading time, loaded heat flux and the number of cycles are presented in Table 1. Depending on the loaded time, components were tested up to 3.4 MW/m<sup>2</sup>. Independently of loading time, no delamination or surface increase temperature was observed during HHF tests. Surface micrographs of these coating are presented in Fig. 4a. Open pores, observable from the top surface, indicate that the coating is not fully dense (~ 95 %) [15]. For this reason the last step of the development consisted in: optimizing the coating density, covering upper divertor prototypes with optimized coating and HHF testing of coated prototypes under relevant WEST heat loading.



Fig. 3 : Size and shape of large inertially cooled component



Fig. 4 : Coatings surface micrographs a) before and b) after coating optimization for 15 µm and 30 µm

# 3.2.3 Optimization of the coating density

Optimization of the coating density consisted in defining the adapted coating process conditions (pressure and duration) for targets and substrate cleaning. If the density is increased, the thermal conductivity of the coating is increased, however the stress at the interface between coating and CuCrZr substrate is increased, so that probability of coating delamination increases. Coating process conditions were defined as a compromise between: obtaining dense coating and obtaining a coating able to sustain the WEST heat loads.

For the optimized coating, fewer pores are visible from the surface compared to the non-optimized coating, (Fig. 4b). The coating cross section presents a columnar growth aspect (Fig. 5). Coating density was not measured; however, considering micrographs, one can say that density was improved with optimization. Coating thicknesses of 18  $\mu$ m and 28  $\mu$ m are obtained for the specified 15  $\mu$ m and 30  $\mu$ m processes, respectively. Obtained nominal thicknesses are  $\pm$  3  $\mu$ m within thickness specification.

### **3.2.4 Prototypes HHF testing (Steady state and transient heat loads)**

In order to evaluate the thermo-mechanical behavior under the temperature distribution relevant to the WEST operation condition, two actively cooled Cu-PFU prototypes (with W coating thickness ~15  $\mu$ m and 30  $\mu$ m), were HHF tested in the GLADIS facility. The heat flux distribution is a Gaussian profile in x and y direction. Applied hydraulic conditions were: 0.4 l/s (corresponding to a water velocity of ~ 8 m/s), 10 bar and 20 °C. Prototypes were loaded by a repetitive cycling: ~ 60 cycles at 8 MW/m<sup>2</sup> followed by ~ 10 cycles at 10.5 MW/m<sup>2</sup>. At a maximum heat flux of 8 MW/m<sup>2</sup> and 10.5 MW/m<sup>2</sup>, a maximum surface temperature of ~ 445 °C and ~ 500 °C, respectively, were obtained by infrared measurements (Table 2). This result is in agreement with the predictions provided by finite element calculations performed for similar heat load conditions showing hence a good thermal behavior of both tested components, and with the expected thermal exhaust capability. No visible delamination is observed after steady state thermal loading even by observations with stereo microscope at 50x magnification.

Then, transient thermal loads (400 kJ/m<sup>2</sup>), similar to those expected during WEST Edge Localised Modes (ELMs) were also applied for the prototype covered with 15  $\mu$ m tungsten coating. To simulate these loads with GLADIS neutral beam, a maximum heat flux of 20 MW/m<sup>2</sup> was applied during t= 20 ms. The beam is then stopped during 40 ms. ELM pulses are repeated during 10 s. Then the beam is stopped during 60 s. This sequence is defined to one cycle. 20 cycles were applied. After this sequence, no delamination was observed.

Considering the fact that no delamination was observed, 15  $\mu$ m and 30  $\mu$ m tungsten coating thicknesses were qualified. However, in WEST, coatings will be loaded for a significantly higher number of cycles than the one tested during this validation program. In order to decrease the risk of delamination driven by repeated thermal stresses in the coating, it was decided to cover the upper divertor and baffle targets with the thinnest coating option (15  $\mu$ m), as it has been calculated to be sufficient to ensure a reasonable lifetime during plasma operation. Indeed, it has been shown that thicker coatings are prone to delamination. This can lead to operational issues limiting plasma performance (see [16] [17] [18] as shown in analysis for the coatings of the JET ITER Like Wall, although on a carbon substrate).



Fig. 5 : Micrographs of coating thicknesses (15  $\mu m$  and 30  $\mu m)$ 

Thickness (# of prototypes)	Absorbed heat Flux (MW/m <sup>2</sup> )	Maximum surface temperature (°C)	Result
15 µm (1)	8 MW.m <sup>-2</sup>	~445	Qualified
30 µm (1)	10.5 MW.m <sup>-2</sup>	~500	Qualified

Table 2: Experimental results for Cu-PFU prototypes under HHF tests

# 4. Conclusion and perspectives

Since 2012, coating developments on CuCrZr substrate have been carried out in order to qualify a tungsten coating suitable for WEST coated actively cooled plasma facing components such as the baffle unit and upper divertor component. The validation program was divided in several phases. The first step enabled to assess the relevant thickness. The second step enabled to validate the coating process with regard to its uniformity and its thermal behaviour under HHF tests. The last step was dedicated to the HHF tests up to 10.5 MW/m<sup>2</sup> on relevant coated actively cooled prototypes. Thermal behaviour, non-uniformity and thickness of the coating (15  $\mu$ m and 30  $\mu$ m) correspond to specifications. As no delamination was observed, coatings of 15  $\mu$ m and 30  $\mu$ m were validated with regard to their application on upper divertor component and baffle unit. Within this validation program, the number of tested samples and components is low. This study is, consequently, not statistically representative. In order to decrease the risk of coating delamination under thermal loads, it was decided to cover the upper divertor and baffle targets with the thinnest coating option of 15  $\mu$ m. Further tests will be performed on this coating to assess the number of fatigue cycles they can undergo at the nominal heat flux specified by WEST operation. In order to avoid starting from scratch after possible tungsten delamination, coating reparation process is also under development.

## 6. References

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