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ABSTRACT

The degradation of polymer light-emitting diodes (PLEDs) under current stress is governed by the formation of hole traps. The presence of traps is reflected in the low-frequency response of PLEDs by a negative contribution to the capacitance that originates from trap-assisted recombination. Since the relaxation time scales with the (inverse) concentration of traps, impedance spectroscopy measurements allow for a quantitative determination of the amount of traps formed during degradation. We demonstrate that the obtained hole trap concentration is in agreement with the amount found by numerically modeling the increase in the PLED driving voltage. Impedance spectroscopy measurements are therefore useful as an *in-situ* characterization tool during PLED degradation, providing information on trap formation without numerical device modeling.

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Polymer light-emitting diodes (PLEDs) are attractive candidates for large-area applications as lighting and displays due to potential low-cost fabrication by coating and printing techniques. A bottleneck for their further development has been their relatively low stability and lifetime. Typically, when stressed under a constant current, PLEDs exhibit a continuous increase in operation voltage and a decrease in luminance.¹ As a possible degradation mechanism, photo-oxidation of the polymer backbone has been proposed.^{2–8} Such photo-oxidation could be initiated or enhanced by the presence of triplet excitons. Triplet excitons not only have a long lifetime but could also activate triplet oxygen to singlet oxygen, which is reactive and could attack the conjugated backbone of the polymer. The electrical characteristics of degraded PLEDs were recently analyzed using a numerical PLED device model.⁹ It was found that the decrease in the PLED current and light-output of degraded PLEDs could be simultaneously explained by the formation of hole traps. Having identified hole trap formation as the main mechanism for degradation enabled us to further investigate the dynamics of trap formation under current stress.¹⁰ For this purpose, numerical simulations were performed that linked the PLED voltage increase during stress directly to the amount of hole traps. It was found that after a short linear increase in the amount of hole traps with stress time, the hole trap formation is characterized by a square root dependence on time. Furthermore, from the voltage increase, the

luminance decay of the PLED could be predicted. The luminance decay under current stress is linked to the hole trap formation via non-radiative recombination of trapped holes with free electrons. The dynamics of the hole trap formation reveals that the degradation is governed by the interactions between excitons and free charge carriers.¹⁰

For such an in-depth analysis of the electrical characteristics of degraded PLEDs, knowledge of the transport parameters of the pristine polymer is required. Electron and hole mobilities including its field and density dependence, electron trapping, and recombination mechanism are needed to obtain quantitative results using a numerical device model.¹¹ For screening of PLED materials, a more direct determination of the amount of traps formed during stress would be convenient. Impedance spectroscopy (IS) is a powerful technique to study charge transport and recombination in PLEDs. At low frequencies, a negative contribution to the differential capacitance C has been observed.^{12–18} Several explanations were proposed, including bimolecular recombination,¹² electron injection through interfacial states,¹³ accumulation of charges at an organic/organic interface,¹⁴ trap-assisted monomolecular recombination,^{15,16} self-heating,¹⁷ and energetic disorder.¹⁸ In a recent study, it has been proven that the negative contribution to the capacitance in pristine PLEDs originates from trap-assisted recombination.¹⁹ First, it was demonstrated that the

observed relaxation time is nearly equal to the inverse rate for trap-assisted recombination of trapped electrons with free holes. Subsequently, by increasing the amount of electron traps upon addition of a known amount of fullerene molecules, the negative contribution to the capacitance was further amplified and the decrease in the relaxation time was quantitatively in excellent agreement with trap-assisted recombination. As final and definite proof, it was shown that when trap-assisted recombination is suppressed by using trap-dilution, the negative capacitance effect disappears. The direct correlation between the negative capacitance effect and trap-assisted recombination makes IS a potential tool for investigation of trap formation in PLEDs under current stress. In an earlier study on small molecule based organic LEDs (OLED), IS has been applied to monitor charge injection during degradation.²⁰ It was found that under current stress, positive charges accumulated at the interface between the hole- and electron transport layers. The amount of accumulated positive charge was correlated with the rate of degradation. For PLEDs, current-voltage and impedance spectroscopy measurements have been performed on pristine and degraded PLEDs.¹⁰ It was demonstrated that the amount of hole traps obtained from the negative capacitance effect is in agreement with the amount of hole traps obtained by numerically modeling the voltage increase during PLED current stress. In the present study, a quantitative analysis of the negative capacitance effect is provided. It is demonstrated that the reduction of the relaxation time for the negative capacitance effect upon degradation is consistent with the formation of hole traps.

Recombination of electrons and holes results in a recombination current j_r . Its contribution to the capacitance can be related to a time domain response on a voltage step ΔV . It was demonstrated by Ershov *et al.*²¹ that a positive derivative of the transient recombination current dj_r/dt will result in a negative contribution to the capacitance. For a single exponential transient, given by $j_r(t) = -j_0 \exp(-t/\tau_r)$, with j_0 being the prefactor of the recombination current j_r , the capacitance is given by

$$C(\omega) = C_0 - \frac{\alpha \tau_r}{1 + \omega^2 \tau_r^2}, \quad (1)$$

with C_0 being the geometrical capacitance $\epsilon_0 \epsilon_r A/d$, ω the angular frequency, α a proportionality factor that scales with j_0 , and τ_r the relaxation time.²¹ The proportionality factor α is voltage dependent and is proportional to the PLED current.¹⁹ The near-exponential dependence of $\alpha(V)$ is therefore the result of the density- and field dependent charge carrier mobility in the space-charge limited PLED. The relaxation time τ_r is characteristic of the (inverse) rate of trapped assisted-recombination. In a pristine PLED, where the hole transport is trap-free,¹¹ the rate for trap-assisted recombination of free holes with trapped electrons can be approximated by

$$R_{SRH} \approx C_p N_t p/2, \quad (2)$$

with C_p representing the hole capture coefficient, N_t the total amount of electron traps, and p the density of free holes. In this approximation, it is assumed that $C_n = C_p$ and $n \approx p$.

This results in a voltage independent relaxation time τ_r given by

$$\tau_r = \frac{2}{N_t C_p}. \quad (3)$$

Upon aging, hole traps are formed in a PLED.^{10,11} An increase in the amount of hole traps is then expected to amplify the negative capacitance effect due to the increased trap-assisted recombination of free electrons with trapped holes.

In this study, PLEDs based on a poly(p-phenylene vinylene) (PPV) based copolymer, known as “super yellow” (SY-PPV, Merck AG),²² were fabricated and aged under a constant current density of 10 mA/cm² for 3 h, 17 h, and 56 h. The SY-PPV was dissolved in toluene and spin-coated onto a glass/ITO/poly(3,4-ethylenedioxythiophene):poly(styrenesulfonic acid) (PEDOT:PSS) substrate. The device architecture of the PLED is ITO/PEDOT/SY-PPV (100 nm)/Ba (5 nm)/Al (100 nm). Impedance data were taken using an Agilent 4284a RCL meter, with the DC bias swept from −2 V to 5 V, superposed by an AC bias of 100 mV. All the devices were fabricated and measured under an inert N₂ atmosphere.

As shown in Fig. 1, at negative bias, the PLED is fully depleted and the measured capacitance equals C_0 , the geometric capacitance of the device. When the applied voltage exceeds the built-in voltage (V_{bi}) of the PLED, the negative contribution to the capacitance sets in and the capacitance decreases with voltage until it reaches a minimum. At higher voltages, the amount of injected charges strongly increases and charge accumulation regions close to the electrodes are formed, leading again to an increase in the capacitance (the electrodes “grow” into the active polymer layer). In this regime, Eq. (1) is no longer valid. We observe that the negative capacitance effect gets more pronounced with aging and shifts to higher voltages, which indicates that upon aging, the trap-assisted recombination is enhanced. In Fig. 2, the differential capacitance C vs frequency at various bias voltages for the pristine (a) SY-PPV PLED and PLED stressed at a constant current of 10 mA/cm² for 3 h (b), 17 h (c), and 56 h (d) are shown. According to Eq. (1), the negative contribution to the capacitance can be described by $\alpha(V)$, being proportional to the PLED current, and a constant relaxation time τ_r . For the pristine SY-PPV PLED, a τ_r value of 2.7 ms was found, which decreased upon degradation to 2.0 ms (3 h), 1.4 ms (17 h), and 0.95 ms (56 h). We note that a voltage independent τ_r is characteristic of trap-assisted recombination since for bimolecular recombination, τ_r should decrease with voltage due to the increase in carrier density.

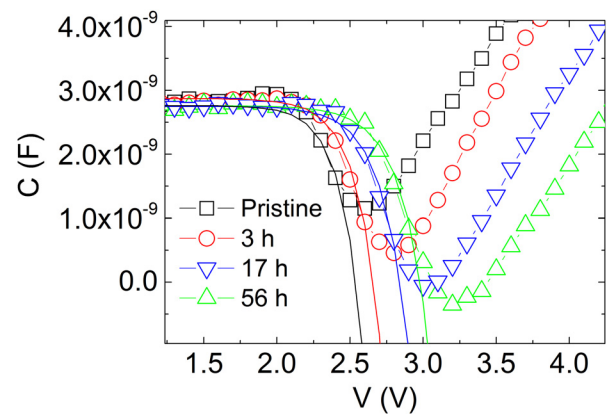


FIG. 1. Measured C-V characteristics (symbols) ($f = 100$ Hz) of a pristine SY-PPV PLED and PLEDs stressed at a constant current of 10 mA/cm² for different aging periods. The PLED active layer thickness amounts to 100 nm. The solid lines are calculated using Eq. (1). The experimental data were taken from Ref. 10, Fig. S2(a).

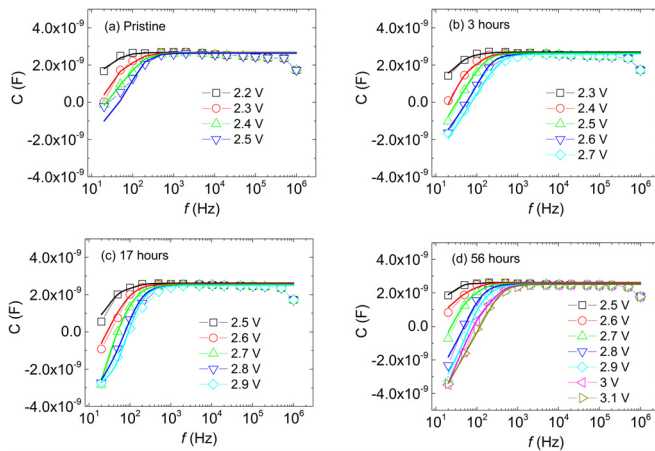


FIG. 2. Differential capacitance C vs frequency at various bias voltages for (a) pristine SY-PPV PLED and (b) PLED stressed at a constant current of 10 mA/cm^2 for 3 h, (c) 17 h, and (d) 56 h. The empty symbols represent the experimental data for an active layer thickness of 100 nm, while the solid lines are a fit to Eq. (1).

The corresponding values of $\alpha(V)$ are shown in Fig. 3 (symbols), together with the normalized measured PLED currents (lines). As reported before for the pristine PLED,¹⁹ it is demonstrated here that also for the aged devices, $\alpha(V)$ is proportional to the PLED current. The obtained $\alpha(V)$ values together with the above-mentioned values of τ_r are then used in combination with Eq. (1) (Fig. 1, lines) to reconstruct the measured C - V characteristics ($f = 100 \text{ Hz}$) of PLEDs aged at a constant current of 10 mA/cm^2 for different periods (Fig. 1, symbols). The calculated C - V characteristics consistently describe the negative capacitance effect upon aging.

The formation of hole traps upon aging leads to additional trap-assisted recombination of free electrons with trapped holes. Since in PPV derivatives, the mobilities of free electrons and holes are equal,¹¹ the capture coefficients C_n and C_p are also equal. As a result, the relaxation time for recombination of free holes with trapped electrons [Eq. (3)] has a similar prefactor as the recombination of free electrons with trapped holes so that both contributions can be simply added. The

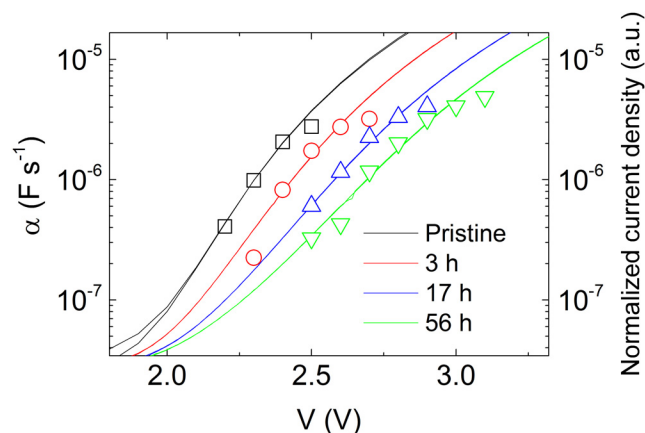


FIG. 3. Voltage dependence of proportionality factor $\alpha(V)$ (symbols) and normalized experimental current densities of pristine and aged SY-PPV PLEDs (solid lines).

relaxation time for aged PLEDs is then governed by the total amount of traps $N_{\text{tot}} = N_{t0} + P_t$, with $N_{t0} = 1 \times 10^{23} \text{ m}^{-3}$ being the density of electron traps in a pristine device⁹ and P_t the amount of hole traps generated during degradation. The total amount of traps N_{tot} can now be directly obtained from the obtained relaxation times of the aged devices using

$$N_{\text{tot}} = \tau_r^{-1}(\tau_0 N_{t0}), \quad (4)$$

with $\tau_0 = 2.7 \text{ ms}$ and $N_{t0} = 1 \times 10^{23} \text{ m}^{-3}$. In Fig. 4, the total amount of traps N_{tot} is shown as a function of the obtained τ_r^{-1} for the pristine and aged PLEDs (black squares). The amount of hole traps upon aging is then obtained from $P_t = N_{\text{tot}} - N_{t0}$ (red dots). We note that for a quantitative determination of the hole trap concentration upon degradation, knowledge of N_{t0} is required, which can be independently determined from charge transport measurements using hole-only and electron-only devices in combination with pristine PLEDs.⁹

In order to maintain a constant current for PLEDs under stress, the generated hole traps, leading to immobile holes in the device, will cause the voltage to rise. From the recorded voltages, the density of hole traps $P_t(t)$ as a function of aging time can be calculated using a numerical PLED device model.¹⁰ In Fig. 5, the numerically calculated amount of hole traps $P_t(t)$ as a function of aging time is shown for a SY-PPV PLED stressed at 10 mA/cm^2 . As reported earlier, the formation of hole traps is governed by linear growth at short aging times and a square-root dependence at longer times.¹⁰ Also included in Fig. 5 are the hole trap concentrations obtained from the relaxation times of the negative contribution to the capacitance, which are shown in Fig. 4 (circles).

We observe that the obtained hole trap concentrations from both methods agree very well. This demonstrates that recombination of free electrons and trapped holes after degradation indeed contributes in an equal way to the negative capacitance as the recombination of free holes with trapped electrons in pristine PLEDs. The quantitative agreement also demonstrates that impedance spectroscopy can be used as a tool to study degradation in organic devices by enabling a direct determination of the trap densities from the observed relaxation times. The only other additional measurement needed is the J - V characteristic of

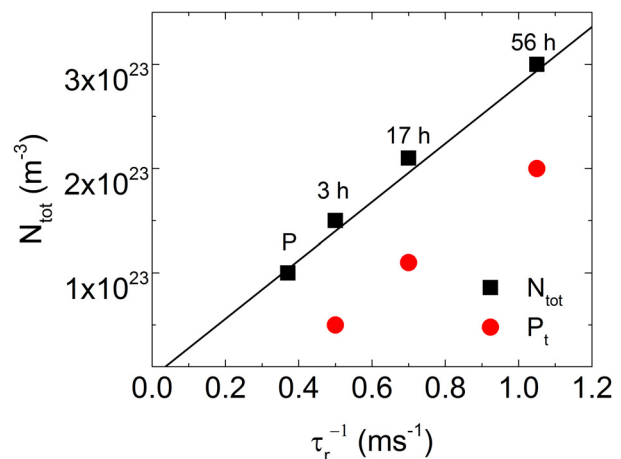


FIG. 4. Total density of traps (squares) and hole traps (circles) for a pristine (P) SY-PPV (no hole traps) and after aging for 3, 17, and 56 h at 10 mA/cm^2 .

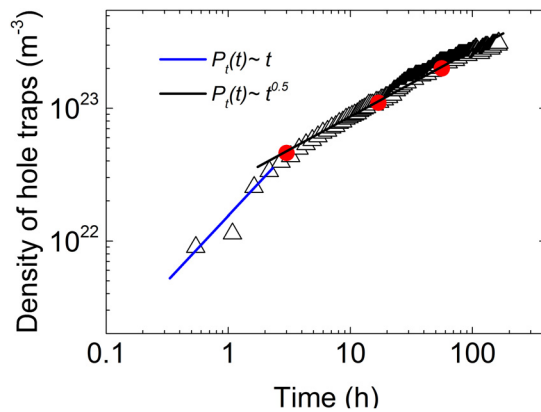


FIG. 5. Density of hole traps formed during degradation at 10 mA/cm² calculated from the voltage increase (open symbols) and the hole trap concentration obtained from capacitance measurements (closed symbols) of a SY-PPV based PLED. The experimental data were taken from Ref. 10, Fig. S2(b).

the degraded PLEDs to incorporate the decrease in the prefactor $\alpha(V)$ upon degradation. For multilayer OLEDs, such a straightforward analysis of IS data is more complicated due to the presence of many interfaces and guest-host layers where charge trapping can occur, which may all affect the frequency response.

In conclusion, we have demonstrated that the enhanced negative capacitance effect in degraded PLEDs, in which hole traps are generated by current stress, can be attributed to trap-assisted recombination of free electrons with trapped holes. From the observed relaxation times, the amount of generated hole traps can be quantitatively determined for various aging times. The obtained trap concentrations are consistent with the hole traps determined from the PLED driving voltage by numerical simulations. As a result, impedance spectroscopy can be used as a tool for monitoring trap concentrations during degradation of organic devices.

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